


## Profile

Name of the Faculty	Ch.Gangadhar					
Designation	Assoc. Professor					
Department	ECE					
Date of Birth	1-05-1975					
DOJ the Institution	16-6-2003					
Employee ID	ECE040071					
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Education Qualifications	UG	PG	Ph.D	Others		
	B.Tech.	M.Tech	VLSI Design			
Work experience	Teaching	Research		Industry	Others	
	22					
Area of Specialization	VLSI Design					
Courses taught at Under Graduate/ Post Graduate Level: (full course title only)						
Electronic Devices & Circuits, VLSI Design, Digital Logic design, Digital IC Applications, VHDL, Verilog HDL, Sensors Technology						
Professional Memberships & ID's : ISSE, ISTE						
Research Publications & Guidance	National Journals	International Journals	National Conferences	International Conferences	Patents	Books / Book chapters
	-	9				
	Masters			-		

<p>Details of Paper Publications (In IEEE Format only Descending order)</p>	<p>Journals:</p> <ol style="list-style-type: none"> <li>1). Spread prediction of covid-19 in andhra pradesh based on environmental chemistry, 806–812, Rasayan Journal of Chemistry, June 2022, 0976-0083, Scopus</li> <li>2). Fibonacci Multichaos Algorithm for Medical Image Encryption for Transmission Through Wavelet Transform Based OFDM System and Its VLSI Realization, 133–139, International journal of online and biomedical engineering, May 2022, 2626-8493, Scopus</li> <li>3). Diagnosis of COVID-19 using 3D CT scans and vaccination for COVID-19, 189–194 World Journal of Engineering, March 2022, 1708-5284, Scopus</li> <li>4). COVID-19 image transmission using convolutional neural networks based algorithms for medical applications, 183–188, World Journal of Engineering March 2022, 1708-5284, Scopus</li> <li>5). Lucas Algorithm for Medical Images Encryption and Transmission using Orthogonal Frequency Division Multiplexing for Medical Health Information Systems and its VLSI Realization, 128–136, International journal of online and biomedical engineering, June 2021, 2626-8493, Scopus.</li> <li>6). Image Compression Using Wavelet Transform and Orthogonal Matching Pursuit, 2289- 2292. Jour of Adv Research in Dynamical &amp; Control Systems, May&amp;2019, 1943-023X, Scopus</li> <li>7). FPGA implementation of OFDM-based mmWave indoor sparse channel estimation using OMP, 2194-2205 Circuits, Systems, and Signal Processing, May&amp;2018, 1531-5878 SCIE</li> <li>8). Discrete Wavelet Transform and Modified Chaotic Key-based Algorithm for Image Encryption and its VLSI Realization, 114-120, IETE Journal of Research April &amp;2012, 0974-780X, SCIE</li> <li>9). HYPERCHAOS BASED IMAGE ENCRYPTION, 3833–3839, International Journal of Bifurcation and Chaos, 3833–3839 November &amp;2009, 1793-6551, SCIE</li> </ol>
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<p>Details of Patents Published / Granted</p>	
	<p>Attended FDP on</p> <ol style="list-style-type: none"> <li>1. 31-7-2023 to 4-8-2023 ,VLSI to System Design: Silicon to End Application Approach, All India Council for Technical Education (AICTE), Arm Education and STMicroelectronics</li> <li>2. 05/02/2024 to 09/02/2024,5G and Beyond , Conducted by NITTTRC &amp; PVPSIT</li> <li>3. 21-9-2023,Simulating Dielectric Variability with the Stochastic Finite Difference Time Domain Method” organized by IEEE IISc MTT/AP-S Student Branch Chapter</li> <li>4. 15-02-2024, ANSYS HFSS SBR+ Antenna Placement. Conducted by ARK Infosolutions PVT LTD</li> <li>5. 24-7-2024,Measuring and Making 2D Materials: From Optics to Epitaxy”Organized by IEEE Sensors Council Student Chapter Indian Institute of Technology Indore</li> <li>6. 11-8-2023,Compacting Models: The Art of Compact Modeling” Organized by IEEE MADHYA PRADESH SECTION EDS CHAPTER</li> <li>7. 1-6-2024, Performance-Specific, Technology-LUT-based Design Methodology for Low-Dropout Voltage Regulators (LDOs)" organized by IEEE-IISc VLSI Chapter</li> <li>8. 15-1-2024,Internet of Bodies for seamless Human-Machine Co-operation and Real-time AI organized by IEEE-IISc VLSI Chapter</li> <li>9. 22-7-2023,NANOSYSTEMS: LAB TO FAB” by Dr. Tathagata Srimani ,organized by IEEE-IISc VLSI Chapter</li> <li>10.3-10-2023,On-Chip Antennas: The Last Barrier to True RF System-on-Chip" organized by IEEE IISc MTT/AP-S Student Branch Chapter</li> <li>11.2-11-2023, Direction of Arrival Estimation with Leaky-wave Antennas at Millimeter Frequencies" organized by IEEE IISc MTT/AP-S Student Branch Chapter</li> <li>12.27-12-2023 to 4-1-2024, One week Workshop on Spartan-3E FPGA Board by PVPSIT &amp; SENSE SEMICONDUCTOR &amp; IT SOLUTIONS PVT. LTD</li> </ol>
<p>Details of Certification Courses / Workshops / STTP’s / FDP (participated / organized)</p>	<ol style="list-style-type: none"> <li>13.25-1-2024,Practical implementation of Artificial Intelligence &amp; IoT in Engineering Solutions, PVPSIT, PHYSITECH CONSULTANCY SERVICES PVT.LTD.</li> </ol>

	14. 21-5-2024 to 22-5-2024, Two Day Workshop titled “Antenna Design for Modern Wireless Applications, PVPSIT& Entuple Technologies
Details of Projects Applied / Awarded	
Any Other information	