Curriculum vitae

Venkata Krishna Gandikota
#3-152/1
Tadepalli (Po)
Guntur (Dt)
Andhra Pradesh

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Career Objective:

To obtain a Full-time position which will provide me with ample opportunities to tread over new avenues and explore a wide range of aspects with my innovative approach and thoughts in the field of Computer Science.

Academic Credentials:

Degree	Institute	Year	Percentage
M.Tech	IIT Kharagpur	2005-2007	7.41/10
M.Sc in Maths	Central University, Hyderabad	2001-2003	58.8
B.Sc	K.B.N.College, Vijayawada	1998-2001	76.5
INTERMEDIATE	S.K.V.S. Jr College, Vijayawada	1996-1998	76.5
SSC	S.R.K.V.High School, Tadepalli	1995-1996	65.5

- GATE PERCENTILE: 98.56 (AIR-22)
- M.Tech in COMPUTER SCIENCE AND DATA PROCESSING I.I.T - (Indian Institute of Technology) -KHARAGPUR

Technical Contour:

SOFTWARE REVELATION
- PRTARAMMING LANGUAGES : C, C++, JAVA
- DATABASE   : SQL
- OPERATING SYSTEMS   : MS-DOS, WINDOWS(NT/XP), UNIX.
- DEVELOPMENT TOOLS   : MS-OFFICE

Relavant Concepts:
Data Structures and Algorithms, Advanced Data Structures, Design and Analysis of Algorithms, Computer Graphics, Object Oriented Programming, Operating Systems, Artificial Intelligence, Compiler Design, Formal Languages and Automata Thoery, Computer Networks, DBMS, Programming Languages, Mathematical Foundations of Computer Science,Operational Research.

M.Tech. Project: Computations of Maximal Ideals in an arbitrary finite ring.
Advisors :   Prof. S. P. Pal (Department of Computer Science and Engineering, lIT Kharagpur),
                     Prof. G. P. Raja Sekhar (Department of Mathematics, IIT Kharagpur).
Description:
My project work is related to Algebra in general and in particular computations of maximal ideals for some of the finite fields. In this, I got the formal proofs on calculating maximal ideals of rings like Z_n, P(x) (the power set ring) and Tensor products, but also the corresponding algorithms are implemented and tested for reasonably large input size. Computations of maximal ideals do have potential applications in fields like Cryptography and Coding Theory.

Accomplishments:
1. Secured 5th rank in Mathematical Olympiad organized by Andhra Pradesh teachers association.
2. Secured All India 17th rank in Hyderabad Central University Entrance Examination for PG.
3. Secured All India 22nd rank in GATE 2004.

Experience:
1. Worked as Lecturer in T.J.P.S College, Guntur from July-2003 to till April -2005 in the department of Mathematics.
2. Worked as an Asst. Professor in Tirumala Engineering College, Hyderabad from July 2007 to till Nov-2010 in the department of CSE.
3. Worked as an Asst. Professor in DVR & Dr HS MIC College of Technology, Kanchikacherla from Dec-2010 to June-2015 in the department of CSE.
4. Conducted Various Finishing School Programs like Logical Reasoning, GATE classes in DVR & Dr HS MIC College of Technology, Kanchikacherla.
5. Worked as an Associate Professor in PSCMR CET, Vijayawada from July 2015 to January 2016 in the department of CSE.
6. Worked as Freelancer GATE Faculty in MADE EASY (Delhi, Kolkata, Pune, Jaipur) from February 2016 to July 2023.
7. Working as an Asst. Professor in PVP Siddhartha Institute of Technology, Vijayawada from 23rd August 2023 to till date in the department of CSE (AI&ML).

Total Experience: 18 years 6 months

Personal Strengths:
- Good creativity, problem solving abilities and communication skills.
- Assertive, self-motivated and positive in attitude.
- Good Team entrepreneur and Quick adaptability to latest technologies.
- Voracious appetite for taking up new ventures and events.

Workshops/ Faculty Development Programs Attended:

1. Participated in Faculty Development Program at Tirumala Engineering College, Bogaram from 5th September to 6th September, 2008.

2. Participated in one day Seminar on Cloud Computing at in DVR & Dr HS MIC College of Technology, Kanchikacherla on 8th January, 2011.

3. Participated in a Mission 10X workshop cnducted at DVR & Dr HS MIC College of Technology, Kanchikacherla from 21st to 25th November 2011.

4. Participated in Faculty enablement program held at RISE group of Institutions, Ongole from 25th to 29th June 2012 conducted by Infosys Ltd.

5. Participated in a Two-week ISTE STTP on "Introduction to Design of Algorithms" Conducted by IIT Kharagpur from 27th April to 30th May, 2015.

6. Participated in Professional Development Programme on "Outcome Based Education" conducted by National Institute of Technical Teachers Training and Research,Chennai at V.R.Siddhartha Engineering College,Vijayawada from 05.09.2023 to 09.09.2023(One Week).

7. Participated in Faculty Development Program on "Practical Implementation of Artificial Intelligence and IOT in Engineering Solutions" conducted by Physitech Consultancy Services Pvt.Ltd. in asscociation with PVP Siddhartha Institute of Technology,Vijayawada on 25.01.2024.

Publications:

1. Published a paper titled "Privacy Preservation in Collaborative Data Mining as Goal Oriented Attack Model" in International Journal of Computer Science Trends and Technology(IJCST) Volume 3 Issue 3, ISSN:2347-8578, May-June 2015.

Personal Profile:

FATHER'S NAME   : GANDIKOTA VENKATESWARLU
MOTHER'S NAME   : GANDIKOTA MAHALAKSHMI
DATE OF BIRTH   : 10 JUNE 1981
GENDER   : MALE
STATUS   : MARRIED
LANGAUGES   : ENGLISH, TELUGU

PERMANENT ADDRESS :   C/O-G.Venkateswarlu,
                           Post&Mndl: Tadepalli,
                           Dist: Guntur,
                           Andhra Pradesh, Pin: 522501.

Declaration

I hereby declare that the information furnished above is true to the best of my knowledge and belief, willing to work in your institution on regular basis.

PLACE:
DATE: KRISHNA GANDIKOTA